A Survey on Unified Power Quality Conditioner for Power Quality Improvement

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Abstract: This paper presents a review on the unified power quality conditioner. Power system consists of complex networks, where many generating stations are interconnected to load center via long power transmission and distribution networks. Outages and interruptions in utility networks, critical commercial operations lead to financial loss, production loss, idle manpower etc. Thus, a new concept is introduced in distribution system for improvement in performance called as custom power. Performance comparison of various custom power devices is done and we can see that for the enhancement of power quality Unified power quality conditioner (UPQC) is better due to its fast response, nominal cost and high reliability. A UPQC usually compensate distortion, unbalanced conditions of voltage and current. It can efficiently protect sensitive loads against the voltage variations or disturbances. UPQC consists of two converters which are connected to a common DC link with an energy storage. UPQC has various configurations based on its application. **Keywords:** UPQC, DVR, D-STATCOM, Power Quality, Distribution System, and APF.

I. Introduction

Power quality is a very important issue in distribution system. Power quality is simply defined as a quality of electricity i.e. it is a concept that is use to describe the purity of the transferred energy. As per IEEE Std. 1100 Power quality is defined as concept of powering and grounding sensitive equipment in a manner that it is suitable for the satisfactory operation of that equipment. But, the power quality is disturbed or destroyed due to several problems occurring in the electrical network for e.g. voltage sag, Voltage swell, unbalance of voltage and current, harmonics produced in voltage and current flickers transient over voltage supply interruption etc. The allowable source voltage and load current distortion (THD) limit as per IEEE standards must be strictly maintained. Harmonic current components create several problems like:

- . Increase in power system losses,
- . Overheating and insulation failures in rotating machinery,
- . Overheating and insulation failures of conductor, transformers, and cables,
- . Reactive power burden,
- . Low system efficiency,
- . Poor power factor,
- . System unbalances and causes excessive neutral currents,
- . Malfunctioning of the protective relays and untimely tripping.

In these mentioned issues regarding power quality are reduced by using filters. There are two types of filters one is passive filter and second is active filter. Now a day's Active power filter is more popular because it's a more attractive compared with passive ones due to its smaller size, fast response and higher performance. The Active Power Filter (APF) have solve the problem regarding current, voltage and the power factor improvement.

The modern power distribution system is becoming highly vulnerable to the different power quality problems stated above [2], [3] The extensive use of nonlinear loads is further contributing to increased current and voltage harmonics issues. Furthermore, the penetration level of small/large-scale renewable energy systems based on, fuel cell, solar energy, wind energy etc. installations at distribution and transmission networks is increasing significantly. This integration of distributed generation in a power system is introducing new challenges to the electrical power industry to accommodate these systems [4]. For distribution system, UPQC is a most attractive solution for compensating power quality problems.

Research is continued for use of UPQC to solve problems such as voltage sag, voltage swell, voltage, correction of power factor and unacceptable levels of harmonics in the current and voltage. The swells are more destructive in nature than sag, UPQC is being studied for mitigation of voltage sag and swell [5], [6]. For example, breakdown of components or equipment due to excessive over voltage during swell condition [7]. In

[8] hysteresis current control method is used to construct APF. A simple Proportional-Integral(PI) controller is brought in use in order regulating the average dc bus voltage which thereby make the reference supply current peak value and supply voltage in phase and the model is tested with different linear and nonlinear loads to remove the harmonics and reduce reactive power. In [9] the technology based on unit vector template generation from distorted input supply is used for solving problems related with voltage and current harmonics in a basic UPQC model. H. Akagi et al. [10] proposed the instantaneous active and reactive power concept. It describes a instantaneous reactive power compensators, here switching devices are used instead of energy storage device. It is proved that both harmonic currents and fundamental reactive power in transient states can be removed. We understand the advanced control strategy i.e. d-q-o method for compensating the voltage harmonics and hence the voltage signal at series active filter is utilized to find the reference signal for the parallel active filter using p - q theory. Metin Kesler [11] proposed an advanced control method(SRF) to overcome the problems of power quality through a three-phase UPQC under unbalanced load conditions. Its performance was analyzed. The proposed control system helps in improving the power quality at the point of common coupling (PCC) on power distribution sys- tem under unbalanced load conditions and non- ideal mains voltage by compensating the current and voltage harmonics and the reactive power. In [12] we see control strategy is dealing with the series inverter controller where amplitude modulation ratio of series inverter sinusoidal PWM voltage controller is regularly adjusted to follow the actual dc link voltage and not the reference dc link voltage. Yash Pal [13], presents a control strategy for a three-phase four-wire Unified Power Quality (UPQC). A three-phase, four leg VSI is used for shunt APF and a three-phase, three legs VSI used for the series APF. Unit vector template control technique is used to get the for controlling the series APF, while $Icos\phi$ control is used for control of shunt APF. This method ensures, mitigation of voltage and current harmonics, load balancing, voltage swell and sag and voltage dips. This method helps effectively in reduction of computational time and number of sensors. The organization of the paper is as follows: Section 2 explores the different types of custom power devices. Structure of UPQC is described in Section 3, Section 4 describe the topologies and Section 5 describes the control techniques for UPQC. Finally, the conclusion is given.

II. Different Types Of Custom Power Devices

The different custom power devices are SVC, dynamic voltage restorer (DVR), which improves the supply power quality, distribution static compensator (DSTATCOM), compensate for current unbalance and harmonics of non-linear loads, and UPQC combination of DVR and D-STATCOM [16] - [18].

Distribution Static Compensator: - is connected in shunt with distribution feeder with the help of coupling transformer. It is a Voltage Source Inverter (VSI) based Static Compensator Device (STATCOM) used to maintain bus voltage sags at the required level by supplying or receiving of reactive power in the distribution system. The single line diag. of D-STATCOM is shown in Figure 1. The coupling of D- STATCOM is three phase, in parallel to network and load. The purpose of the D-STATCOM is to cancel load harmonics fed to the supply. It works as current sources, connected in parallel with the nonlinear load, it generates the harmonic currents the load requires, balance them, and provide reactive power.

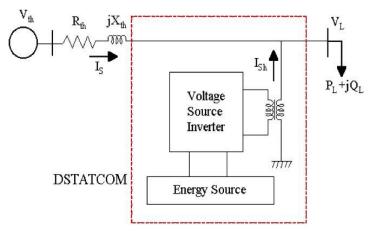


Fig.1: The single line diag. of D-STATCOM.

Dynamic Voltage Restorer (DVR): - It is a controlled, high-speed switching power electronic device. DVR is connected in series, designed to inject a dynamically controlled voltage in magnitude and phase in to distribution line through coupling transformer to correct load voltage. A basic DVR is shown in the Figure 2. It consists of a dc-dc boost-converter, voltage source inverter(VSI), energy storing device, ac filter and a coupling

transformer, connected in series. We can use dc capacitor bank, fuel cell, pv-array, flywheel, supercapacitor, etc. as energy storage device, interfaced to boost converter. Generally dc capacitor bank is used. The dc link capacitor voltage is regulated by boost converter. It is used as a common voltage source for the inverters. The compensating voltage is generated by boost converter, which is injected into distribution system through series matching transformer. The ac filter is used to overcome the switching losses due to control signal generating techniques for VSI and the effects on winding of coupling transformer. The function of a dynamic voltage restorer is to reduce voltage sags which cause damage to the sensitive loads like semiconductor manufacturing plant or IT industry.

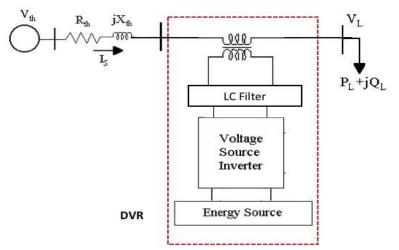


Fig.2. The single line diagram of Dynamic Voltage Restorer.

There are different types of power quality problems in distribution system and to each problem, there is a solution. Table I, shows the overall comparison of three custom devices. It has been shown that a DVR is superior to DSTATCOM. Unified PQ Conditioner (UPQC) is the best protection for sensitive loads from sources with poor quality. It is combination of DVR and DSTATCOM.

S. No	Factors	DSTATCOM	DVR	UPQC
1	Rating	low rating	high rating	higher ratings are available
2	Speed of operation	Less than DVR	Fast	Faster
3	Compensation Method	Shunt Compensation	Series Compensation	Both series & shunt
4	Active /reactive Power	Reactive	Active/ Reactive	Both
5	Harmonics	Less	Very less	Lesser
6	Problems addressed	Sag/ Swell	Sag/ Swell/ Harmonics	Sag/ Swell/ Harmonics/ Flicker/ Transients/ unbalance in 3 phase system
7	Cost	Nominal	High	Higher

Table I: COMPARISON OF VARIOUS CUSTOM POWER DEVICES

III. Unified Power Quality Conditioner

The schematic diagram of a single-phase Unified Power Quality Conditioner is shown in Figure 3. UPQC consists of two IGBT based Voltage source converters (VSC), one shunt and other series which are connected to a common DC link. The shunt converter is connected in parallel to the load. It provides VAR compensation to the load and supply harmonic currents. Whenever there is sag in supply voltage then series converter injects suitable voltage to overcome sag in supply. Thus, UPQC improves the power quality by mitigating problems due to load current harmonics and by power factor correction.

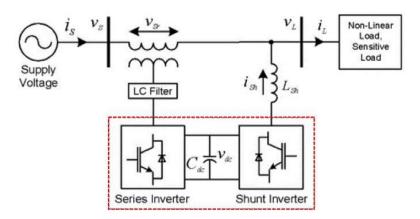


Fig.3: UPQC basic Structure.

The key components of the system are as follows:

a) **Series converter** is a VSC connected in series with the AC supply line. It acts as a line voltage source to compensate voltage disruptions. It is used to minimize line voltage fluctuations from the load supply voltage and feeds to shunt branch of the device to consume current harmonics produced by unbalance load.

b) **Shunt converter** is a VSC which is connected in parallel with the AC supply line. It acts as a current source to eliminate current disruptions and eliminates the reactive current in the load circuit. It improves the power factor of load and acts as DC-link voltage regulator for the reduction of the DC capacitor rating.

c) **Energy storage** The DC capacitor bank is generally used. it is connected between Midpoint-to-ground is divided into two parts, which are arranged in series together. The neutral point's secondary transformer is connected to the DC link midpoint directly. Since both three-phase transformers are connected in Y/Yo form, the zero- sequence voltage will appear in primary winding of transformer which is connected in series to mitigate the zero-sequence voltage of the supply power system. There would not be any zero- sequence current flow in the primary side of both transformers. When the voltage disturbance occurs the system current is balanced. Various other energy storage devices can be used such as batteries, superconducting coils, supercapacitors, flywheels, etc.

d) **The Low-pass filter (LPF)** Due to high-frequency switching mode high frequency components are produced at the output side of series converter to attenuate these LPF is used.

e) **High-pass filter (HPF)** In current switching mode ripples produced can be consumed by applying HPF at the output of shunt converter.

f) **Series and shunt transformers** are used to inject the compensating voltages and currents for electrically separation of UPQC converters.

IV. UPQC Topologies

Power electronics device has many advantages and disadvantages. But the UPQC is the most powerful power electronics device for heavy loads and have high sensitivity towards disruption in line voltage and load current. UPQC is more flexible than any single Converter/inverter based device. It can correct the imbalance and disturbances in the supply voltage and current where as other devices perform any one function. The basic topologies of UPQC are shown in Figure 4.

Right Shunt (UPQC-R) & Left Shunt (UPQC-L) UPQC: This configuration is based on the position of series and shunt inverter connections, as UPQC consist of two back-to-back converters. UPQC-R: If the shunt inverter is connected on the right side of the series inverter [19], [20], [21], [22], [23]. UPQC-L: If the shunt inverter is connected on the left side of the series inverter [19], [24], [25], [26], [27], [28].

Open UPQC: It is a power-electronic unit having steady state performance installed in series with the medium-voltage/low-voltage (LV) substation, along with several power-electronic shunt units connected close to the end users. Dc common link is not present in this configuration, so different control techniques are employed. This device can improve power quality by using only the series unit, all customers connected/supplied by the mains are supplied with custom power [31].

Interline UPQC (UPQC-I): Fig.4(D) depicts an interesting UPQC system configuration, suggested by Jindal et al. [29], in Interline UPQC (UPQC-I) the two inverters of the UPQC are connected between two distribution feeders. One of the inverters is connected in series with one feeder while the second inverter in shunt with second feeder. This configuration can regulate the voltages of both the feeders simultaneously.

Multilevel UPQC (UPQC-ML): Rubilar et al. designed a multilevel UPQC based on a three-level neutral point clamped (NPC) topology [30]. Fig.4(E) shows a Multilevel-UPQC system configuration. A three-level topology requires double semiconductor devices (24) as that of the two-level UPQC system. Based on the requirements, the UPQC-ML can be designed for several levels such as 3-level, 5-level, 7-level and so on.

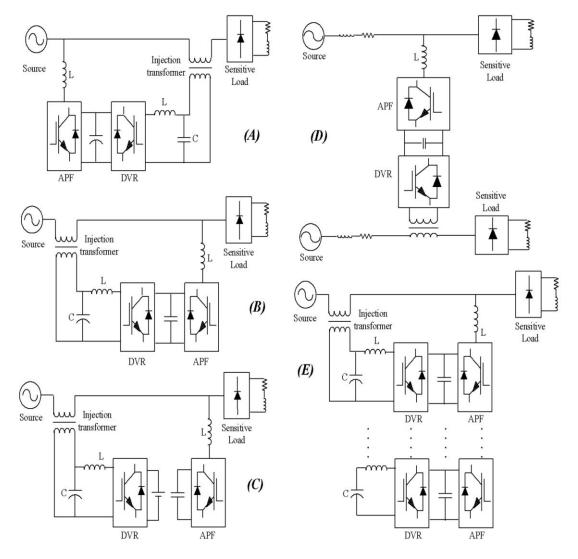


Fig. 4. Power circuit topologies. (A) Left shunt-UPQC, (B) Right shunt-UPQC, (C) Open-UPQC, (D) Interline-UPQC, (E) Multilevel-UPQC [32].

Classification of active power filter is shown in Figure 5. VSI refers to Voltage Source Inverter and CSI refers to Current Source Inverter. UPQC-MC refers to Multi-converter topology. The system is extended by adding a series-VSC in an adjacent feeder. The proposed topology can be used for simultaneous compensation of voltage and current in both feeders by sharing power compensation capabilities between two adjacent feeders which are not connected, UPQC-MD refers to Modular topology in which H-bridge module are used for capacity enhancement, UPQC-D refers to distributed, and UPQC-DG refers to UPQC for distributed generation integrated UPQC. DG sources are connected to a DC link instead of energy storage device in the UPQC as an energy source.

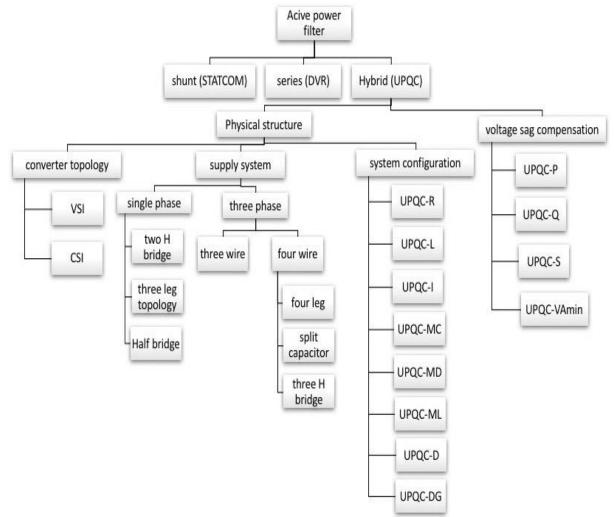


Fig. 5. Classification of UPQC.

Now a day's, due to increase in energy supplied by renewable energy sources, the most interesting topology is UPQC it is being studied for challenges in integrating distributed generation to Micro-grid.

V. Control Of UPQC

Control strategy play very important role in system's performance. The control strategy of UPQC follow three steps. Firstly, sensing the voltage signals by using power transformer or voltage sensor and current signals by using current transformer or current. In second step derivation of compensating commands in terms of voltage and current levels. The instantaneous active and reactive power (p-q theory), and Synchronous reference frame method (d-q theory) are the most widely used time domain control techniques for deriving compensating commands. The third step is, the generation of gating signals for semiconductor switches of UPQC using PWM, hysteresis or fuzzy logic based control techniques.



•• It can be done using voltage/current sensors or power /current Transformer for voltage /current signal.

•• Frequency Domain Control Techniques •• Fast Fourier transfor

•• Time Domain Control Techniques

•• Instantaneous active & reactive power or 3phase p-q theory Synchronous reference frame (SRF)or 3phase d-q theory,Unit Vector Template Generation (UVTG),One Cycle Control (OCC), H∞-based model matching control, Model Predictive Control (MPC), Deadbeat Control, Artificial Neural Network (ANN) technique, Feed forward & feedback theory, Multi Output ADAptive LINear Approach (MO- ADALINE).

•• Gating signal generation for semiconductor switches of UPQC by using PWM(SPWM, SVPWM) Hysteresis band controller, FUZZY LOGIC control, ANN etc.

VI. Conclusion

With a fast-increasing number of applications of industry electronics connected to the distribution systems today, including non-linear, switching, reactive, single-phase and unbalanced three-phase loads, a complex problem of power quality evolved characterized by the voltage and current harmonics, unbalances, low Power Factor (PF). UPQC can deal with both load current and supply voltage disruptions. This paper presents a review on custom power devices, topologies of UPQC and control techniques used to enhance the power quality. The UPQC can mitigate voltage related power quality issues such as, sags, swells, unbalance, flicker, harmonics, and for load current power quality problems such as, harmonics, unbalance, reactive current and neutral current. UPQC can improve power quality in Distributed Generation & Micro-grid.

References

- X. Li, Y. J. Song, and S. B. Han, "Study on power quality control in multiple renewable energy hybrid microgrid system," in 2007 IEEE Lausanne Power Tech, July 2007, pp. 2000–2005.
- [2]. R. C. D. M. FMcGranaghan and H. W. Beaty, "Summary of distributed resources impact on power delivery systems," *Electrical PowerSystems Quality,McGraw- Hill,New York*, 1996.
- [3]. C. Sankaran, "Power quality," *Boca Raton, FL: CRC Press*, 2002.
- [4]. R. A. Walling, R. Saint, R. C. Dugan, J. Burke, and L. A. Kojovic, "Summary of distributed resources impact on power delivery systems," *IEEE Transactions on Power Delivery*, vol. 23, no. 3, pp. 1636–1644, July 2008.
- [5]. M. C. Cavalcanti, G. M. S. Azevedo, B. A. Amaral, and F. A. S. Neves, "A photovoltaic generation system with unified power quality conditioner function," in *31st Annual Conference of IEEE Industrial Electronics Society*, 2005. *IECON 2005.*, Nov 2005, pp. 6 pp.–.
- [6]. B. Han, B. Bae, H. Kim, and S. Baek, "Combined operation of unified power- quality conditioner with distributed generation," *IEEE Transactions on Power Delivery*, vol. 21, no. 1, pp. 330–338, Jan 2006.
- [7]. H. Fujita and H. Akagi, "The unified power quality conditioner: The integration of series active filters and shunt active filters," in *PESC Record. 27th Annual IEEE Power Electronics Specialists Conference*, vol. 1, Jun 1996, pp. 494–501 vol.1.
- [8]. D. C. Bhonsle and R. B. Kelkar, "Design and simulation of single phase shunt active power filter using matlab," in 2011 International Conference On Recent Advancements In Electrical, Electronics And Control Engineering, Dec 2011, pp. 237–241.
- [9]. V. Khadkikar, P. Agarwal, A. Chandra, A. O. Barry, and T. D. Nguyen, "A simple new control technique for unified power quality conditioner (upqc)," in 2004 11th International Conference on Harmonics and Quality of Power (IEEE Cat. No.04EX951), Sept 2004, pp. 289–293.
- [10]. N. H. Akagi, Y. Kanazawa, "Generalized theory of the instantaneous reactive power in three phase circuits," in *IPEC-Tokyo'83 Int. Conf. Power Electronics, Tokyo*, July 1983, pp. 1375–1386.
- [11]. M. Kesler and E. Ozdemir, "Synchronous-reference-frame-based control method for upqc under unbalanced and distorted load conditions," *IEEE Transactions on Industrial Electronics*, vol. 58, no. 9, pp. 3967–3975, Sept 2011.
- [12]. Axente, M. Basu, and M. F. Conlon, "dc link voltage control of {UPQC} for better dynamic performance," *Electric Power Systems Research*, vol. 81, no. 9, pp. 1815 1824, 2011.
- [13]. S. Yash Pal and B. Singh, "A novel control strategy of three-phase, four- wire upqc for power quality improvement,"

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Journal of Electrical Engineering and Technology, vol. 7, no. 1, pp. 1–8, 2012.

- [14]. Singh, K. Al-Haddad, and A. Chandra, "A review of active filters for power quality improvement," IEEE Transactions on Industrial Electronics, vol. 46, no. 5, pp. 960–971, Oct 1999.
- [15]. N. G. Hingorani y L. Gyugyi. "Understanding FACTS". IEEE Press; 2000.
- [16]. C. Benachaiba, O. Abdelkhalek, S. Dib, M. Haidas, "Optimization of parameters of the unified power quality conditioner using genetic algorithm method," *Information Technology and Control*, vol.36, No.2, pp. 242–245, 2007.
- [17]. O. Abdelkhalek, C. Benachaiba, B. Gasbaoui, A. Nasri, "Using of Anfis and fis methods to improve the UPQC performance," *International Journal of Engineering Science and Technology*, vol. 2(12), pp.6889-6901, 2010.
- [18]. C. Benachaiba, Ahmed M. A. Haidar, M. Habab, O. Abdelkhalek, "Smart Control of UPCQ within Micro Grid Energy System," *ELSEVIER*, Procedia Energy, vol. 6, pp. 503–512, 2011.
- [19]. A. Ghosh and G. Ledwich, Power Quality Enhancement Using Custom Power Devices. Boston, MA: Kluwer, 2002.
- [20]. S. Chen and G. Joos, "A unified series-parallel deadbeat control tech- nique for an active power quality conditioner with full digital implemen- tation," in Proc. IEEE 36th Ind. Appl. Soc. Annu. Meet. Ind. Appl. Conf., 30 Sep.-4 Oct., 2001, pp. 172–178.
- [21]. M. Basu, S. P. Das, and G. K. Dubey, "Experimental investigation of performance of a single phase UPQC for voltage sensitive and non- linear loads," in Proc. 4th IEEE Int. Conf. Power Electron. Drive Syst., Oct. 22–25, 2001, pp. 218–222.
- [22]. A. Elnady and M. M. A. Salama, "New functionalities of an adaptive unified power quality conditioner," in Proc. Power Eng. Soc. Summer Meet., 2001, pp. 295–300.
- [23]. B. S. Chae, W. C. Lee, D. S. Hyun, and T. K. Lee, "An overcurrent protection scheme for series active compensators," in Proc. 27th Annu. Conf. IEEE Ind. Electron. Soc., 2001, pp. 1509–1514.
- [24]. S. Chen and G. Joos, "Rating issues of unified power quality conditioners for load bus voltage control in distribution systems," in Proc. Power Eng. Soc. Winter Meet., 28 Jan.–1 Feb., 2001, pp. 944–949.
- [25]. A. Elnady, A. Goauda, and M. M. A. Salama, "Unified power quality conditioner with a novel control algorithm based on wavelet transform," in Proc. Can. Conf. Electr. Comput. Eng., 2001, pp. 1041–1045.
- [26]. E. H. Watanabe and M. Aredes, "Power quality considerations on shunt/series current and voltage conditioners," in Proc.10th Int. Conf. Harmonics Quality Power, Oct. 6–9, 2002, pp. 595–600.
- [27]. A. Ghosh, A. K. Jindal, and A. Joshi, "Inverter control using output feedback for power compensating devices," in Proc. Convergent Technol. Conf., Oct. 15–17, 2003, pp. 48–52.
- [28]. A. Ghosh, A. K. Jindal, and A. Joshi, "A unified power quality conditioner for voltage regulation of critical load bus," in Proc. Power Eng. Soc. Gen. Meet., Jun. 6–10, 2004, pp. 471–476.
- [29]. A. K. Jindal, A. Ghosh, and A. Joshi, "Interline unified power quality conditioner," IEEE Trans. Power Del., vol. 22, no. 1, pp. 364–372, Jan. 2007.
- [30]. I. Rubilar, J. Espinoza, J. Munoz, and L. Moran, "DC link voltage un- balance control in three-phase UPQCs based on NPC topologies," in Proc. 42nd Ind. Appl. Soc. Annu. Meet. Ind. Appl. Conf., Nov. 5–8, 2007, pp. 597–602.
- [31]. M. Brenna, R. Faranda, E. Tironi, "A new proposal for power quality and custom power improvement: OPEN UPQC," IEEE Trans. Power Del., vol. 24, no. 4, pp. 2107–2116, Nov. 2009.
- [32]. Ahmet Teke, Mehmet Tumay, "Unified Power Quality Conditioner: A Literature Survey," *Journal of Electrical Systems* vol-7, no-1, pp. 122–130, 2011.